

## DSP System Interface

Figure 10-19 shows a simplified ADSP-2189M system using the full memory mode configuration with two serial devices, a byte-wide EPROM, and optional external program and data overlay memories.

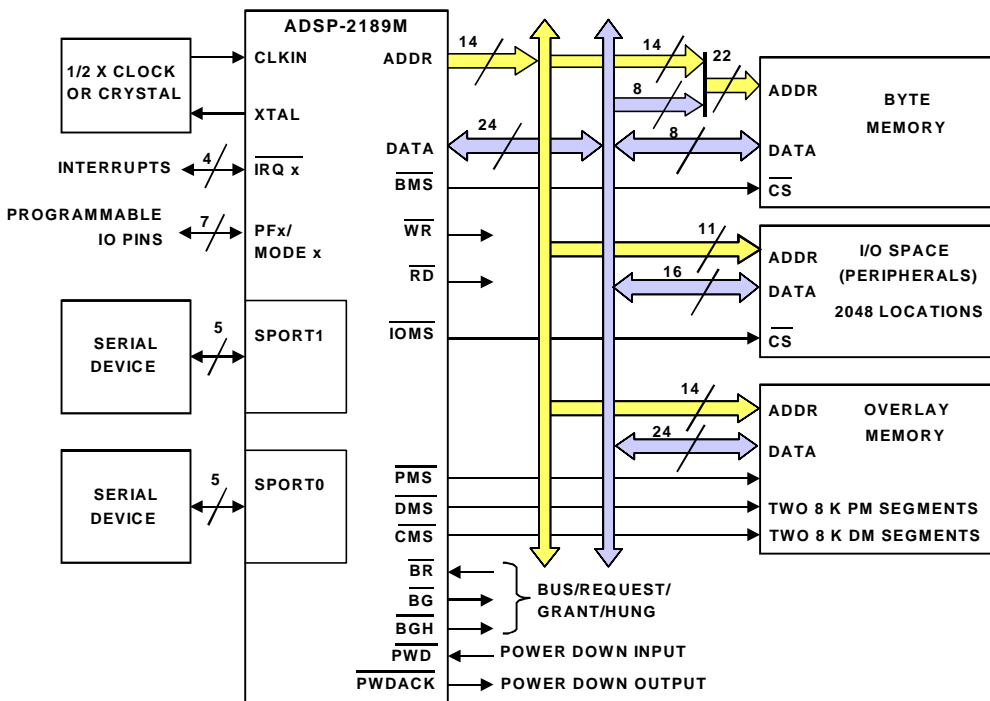


Figure 10-19. ADSP-2189M System Interface (Full Memory Mode)

Programmable wait state generation allows the fast processor to connect easily to slower peripheral devices. The ADSP-2189M also provides four external interrupts, seven general-purpose input/output pins, and two serial ports.