

## Arithmetic Logic Unit (ALU)

Table 2-3. Standard ALU Functions (Cont'd)

Function	Description
R = X XOR Y	Logical Exclusive OR of X and Y operands
R = NOT X	Logical NOT of X operand ( <i>ones-complement</i> )
R = NOT Y	Logical NOT of Y operand ( <i>ones-complement</i> )

## ALU Input/Output Registers

Table 2-4 lists the sources for ALU input and output registers.

Table 2-4. Sources for ALU Input and Output Registers

Source for X Input Port	Source for Y Input Port	Destination for R Output Port
AX0, AX1	AY0, AY1	AR
AR	AF	AF
MR0, MR1, MR2 <sup>1</sup>		NONE
SR0, SR1 <sup>2</sup>		

1 MR0, MR1 and MR2 are multiplier/accumulator result registers.

2 SR0 and SR1 are shifter result registers.